

Abstracts

Investigations on the Reduction of Package Densities in Coplanar Circuits

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One of the advantages of coplanar circuit design over microstrip design is the possibility to achieve higher package densities. The paper presents a modified method to calculate generalized scattering parameters for neighboured coplanar lines. A full-wave method is used to analyze important parameters as insertion loss, isolation and reflection not only for the coplanar mode but also for the conversion to the coupled slot line mode. Results for various structures on a GaAs-substrate are presented.

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